Pransmitted herewith for filing is the Patent Application of:

aventor: KWOK KEUNG PAUL HO AND XUECHUN DAI

PLASMA ETCH METHOD FOR FORMING PLASMA ETCHED SILICON LAYER



Enclosed are:

X 2 sheets of drawing(s) - formal.

X An assignment of the invention to Chartered Semiconductor Manufacturing Ltd.

An associate power of attorney

The filing fee has been calculated as shown below:

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	FOR:	NO. FILED	NO. EXTRA	RATE	FEE
1	BASIC FEE				\$ 760.
in the second	TOTAL CLAIMS	16 -20=	0	x 18 =	\$ 0.
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ļL	MULTIPLE DEPENDENT CLAIM PRESENTED			+ 260 =	
(1) (1)			s	UB TOTAL	\$ 760.
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Respectfully symmitted, STEPHER B. ACKERMAN, REG. NO. 37,761